

FIG. 1A

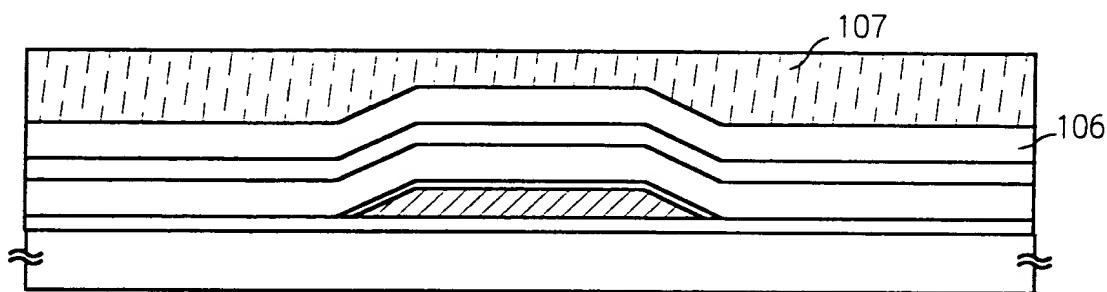


FIG. 1B

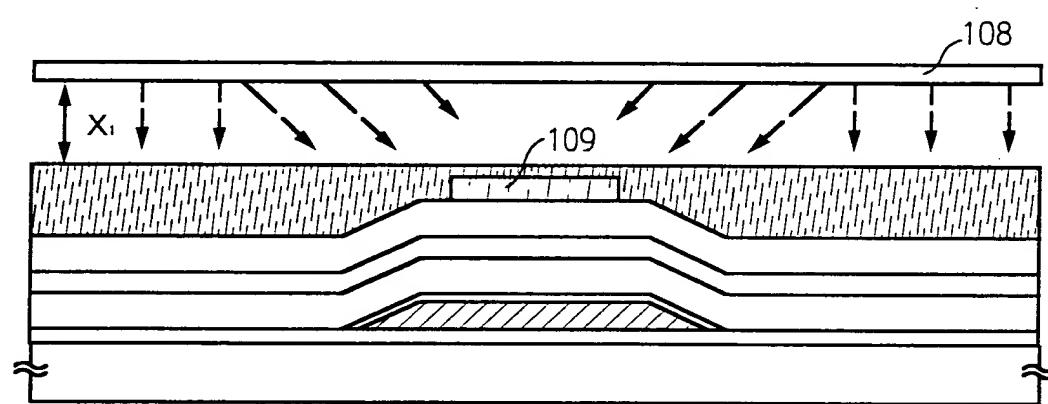


FIG. 1C

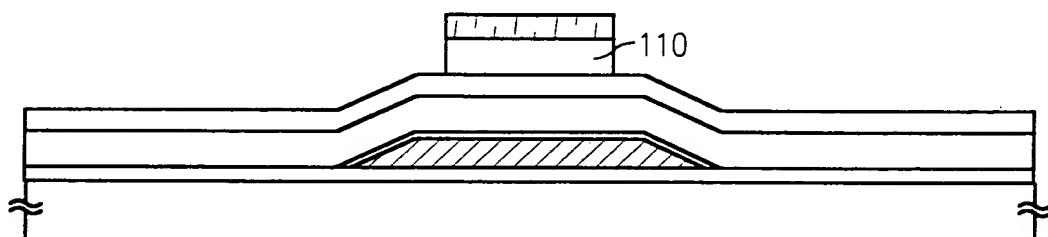


FIG. 1D

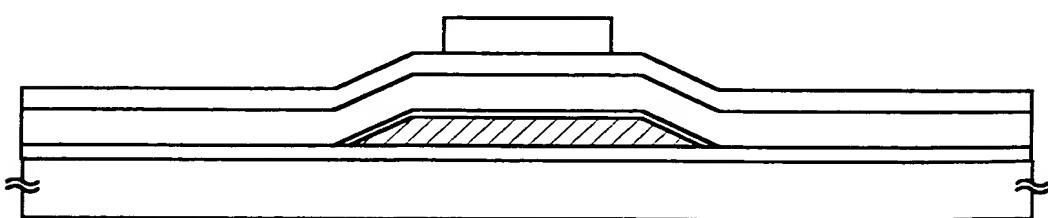


FIG. 1E

FIG. 2A

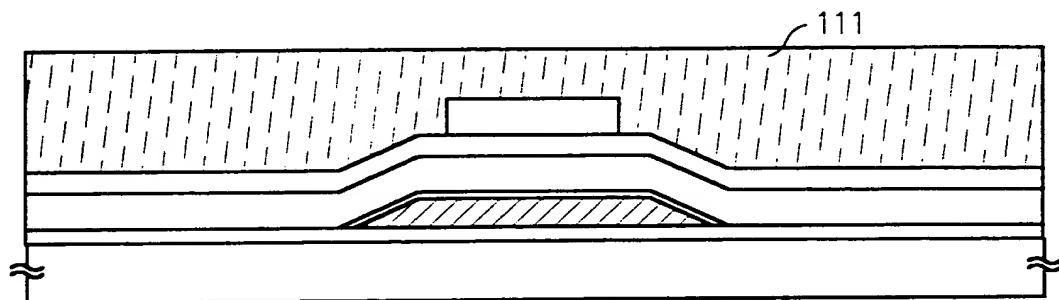


FIG. 2B

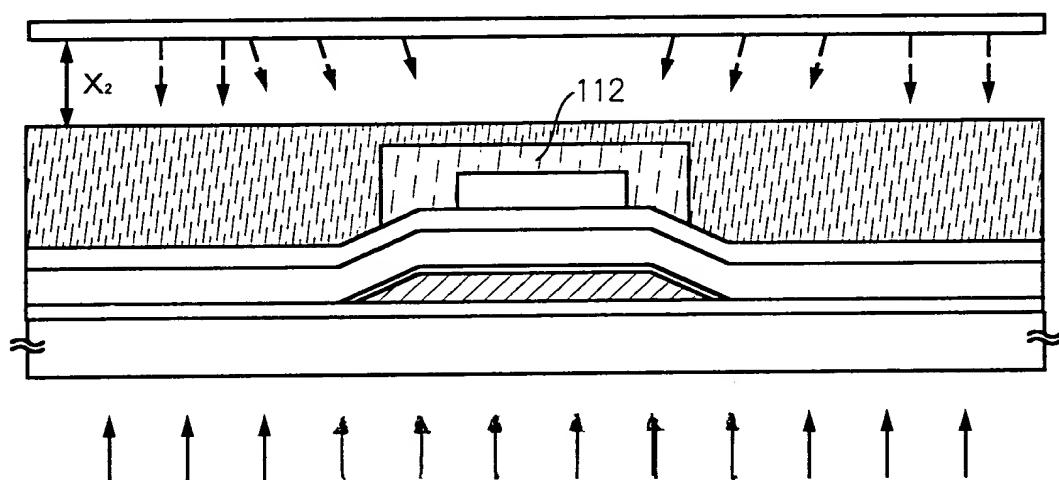


FIG. 2C

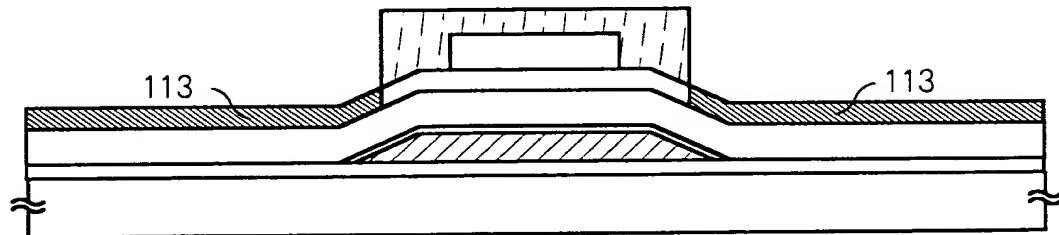


FIG. 2D

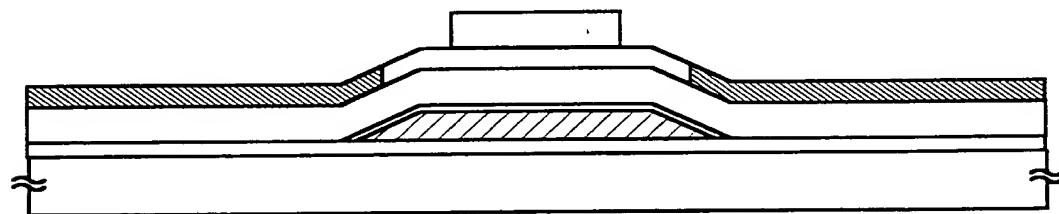


FIG.3A

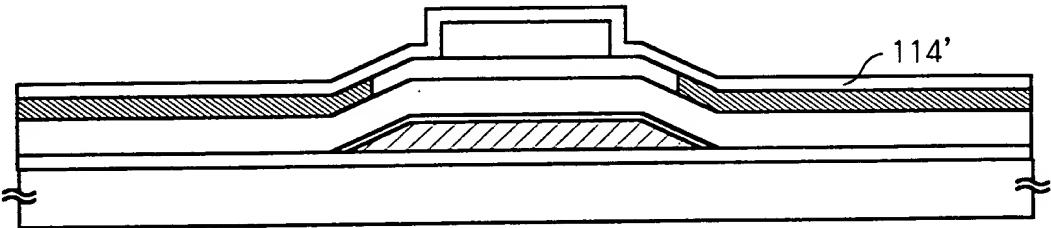


FIG.3B

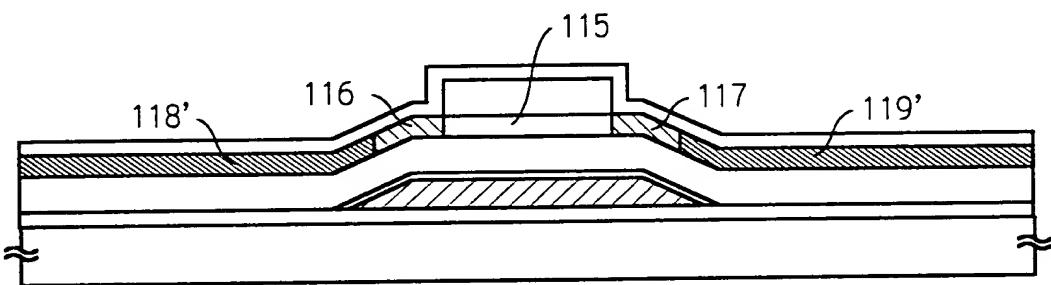
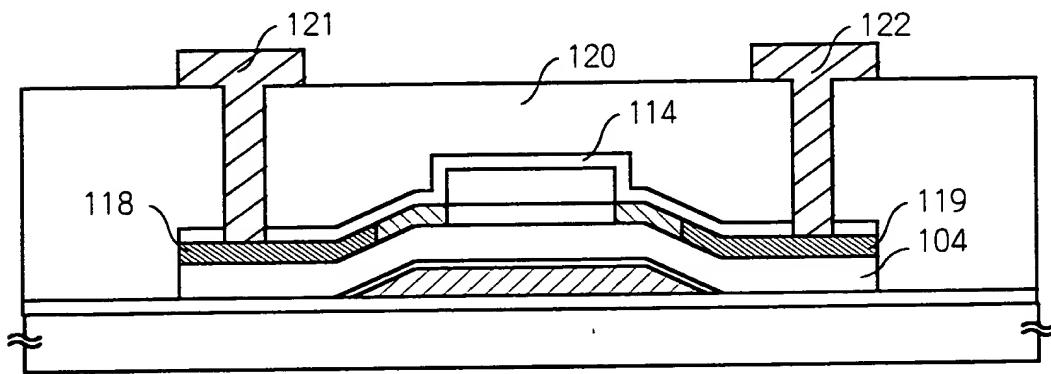


FIG.3C



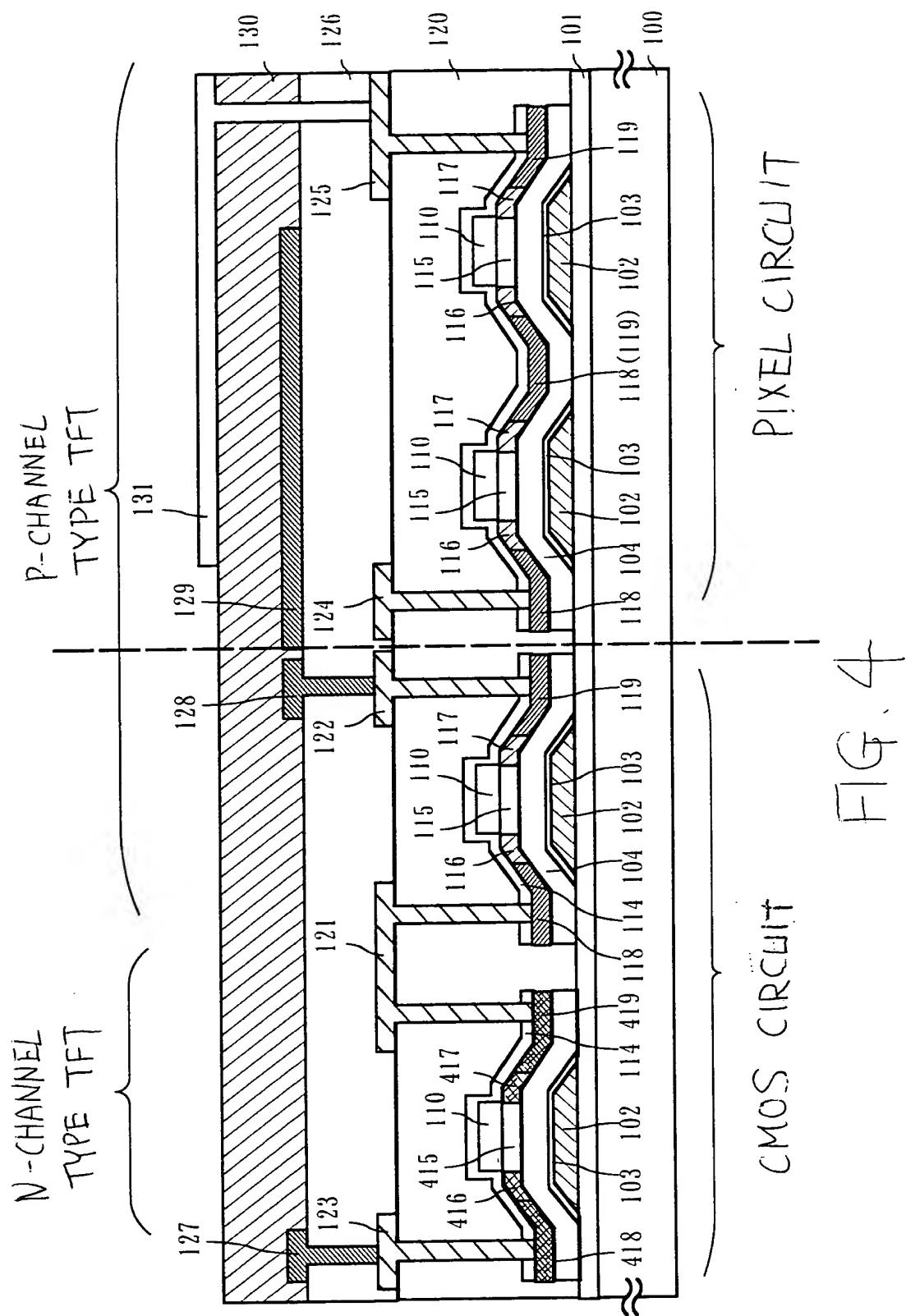


FIG. 4

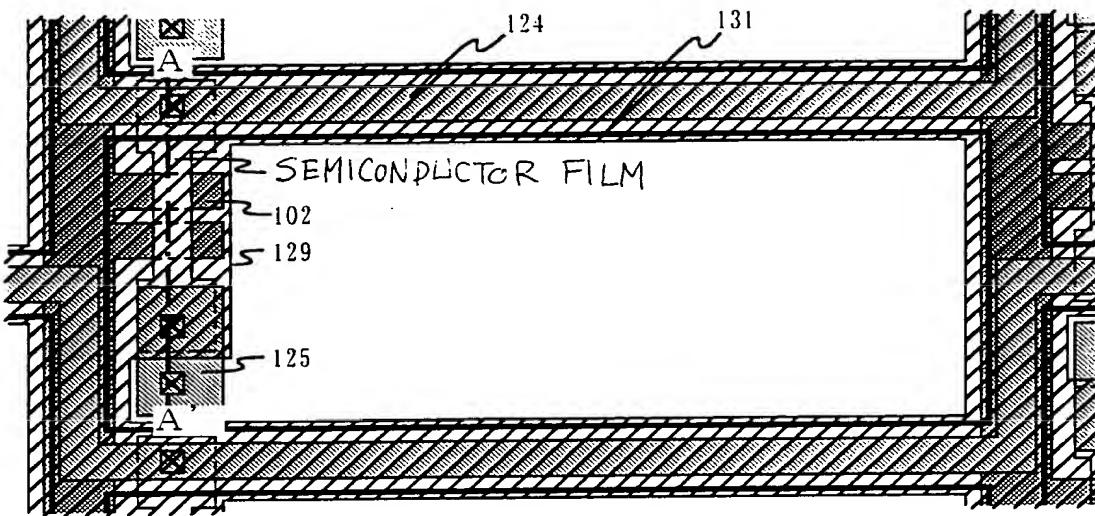


FIG. 5A

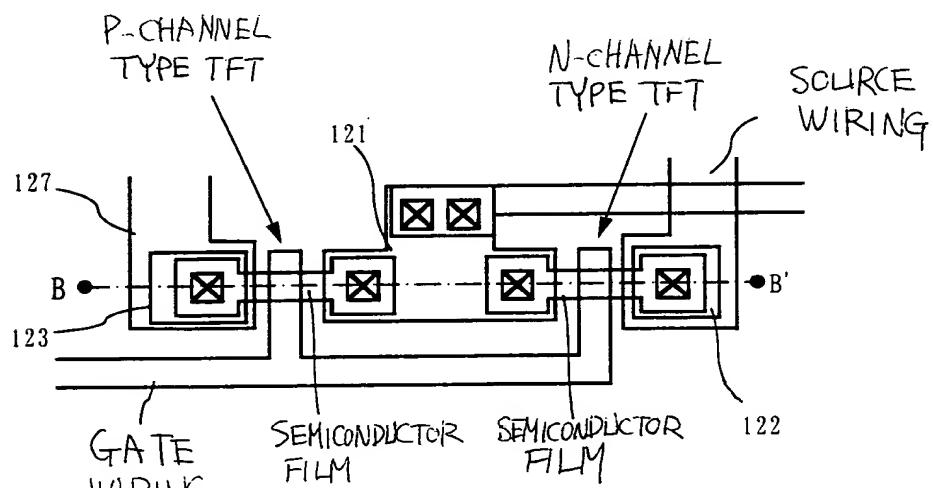


FIG. 5B

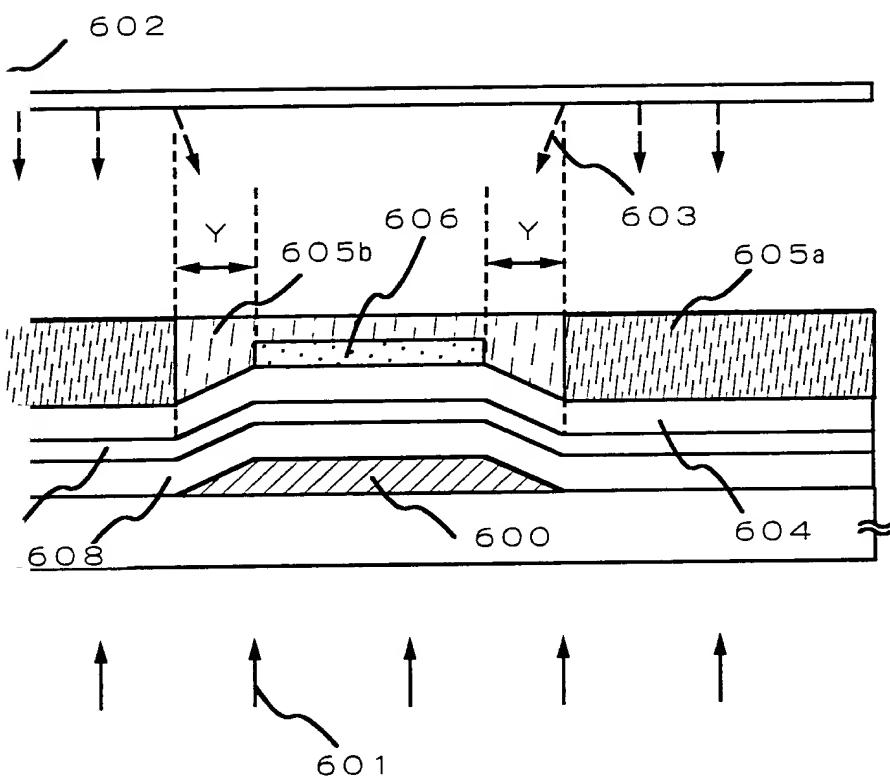
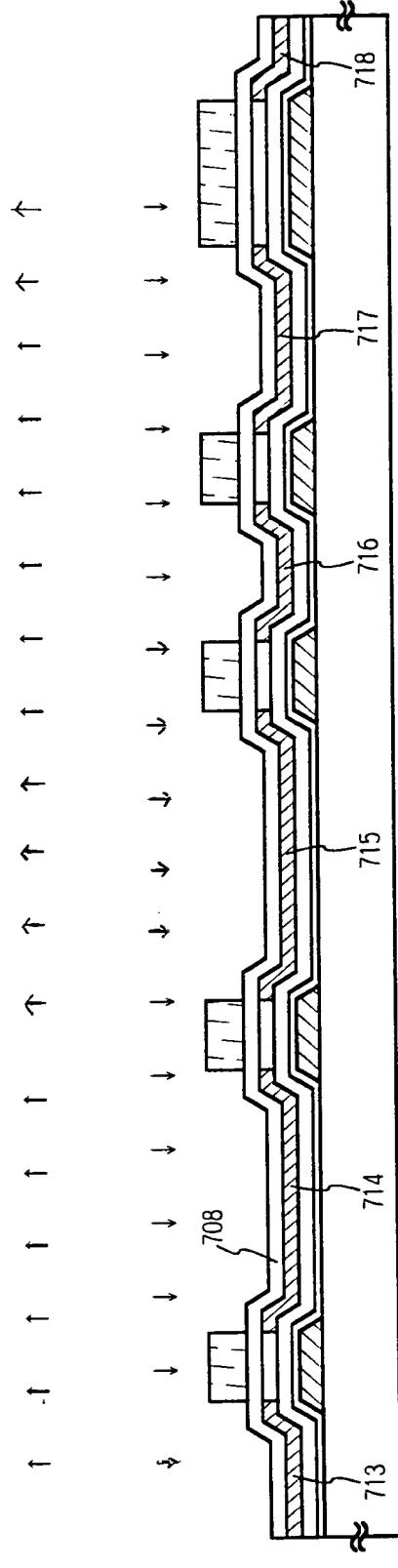
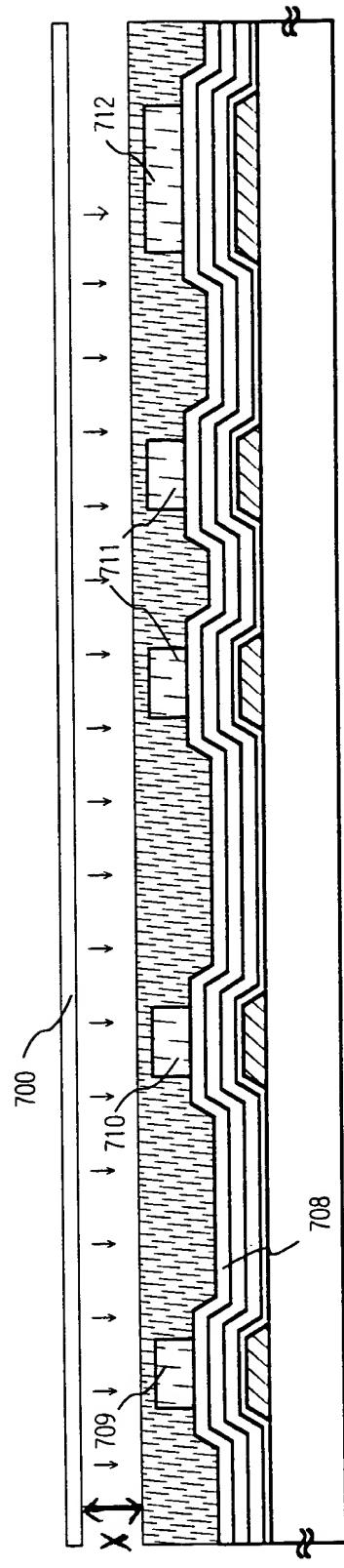
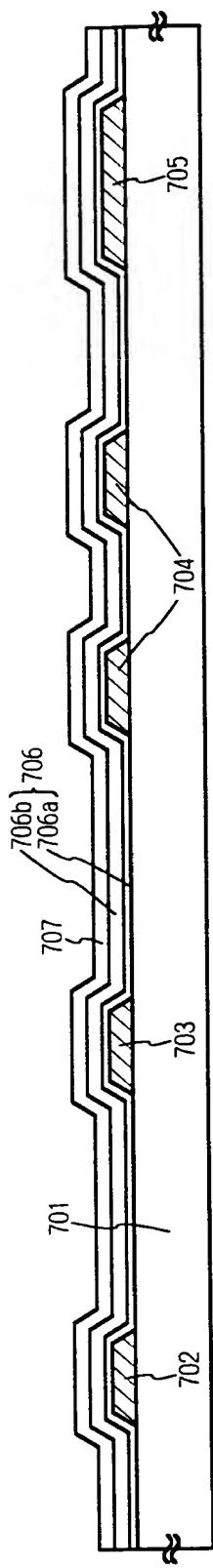
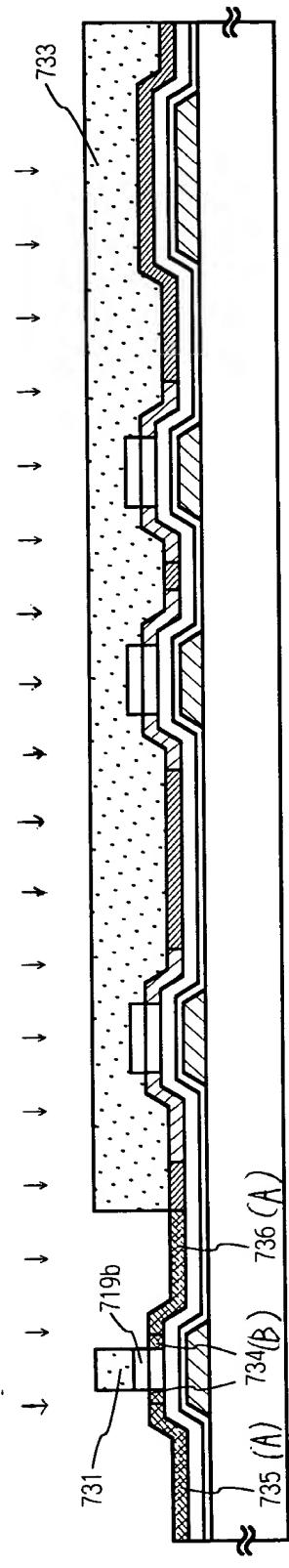
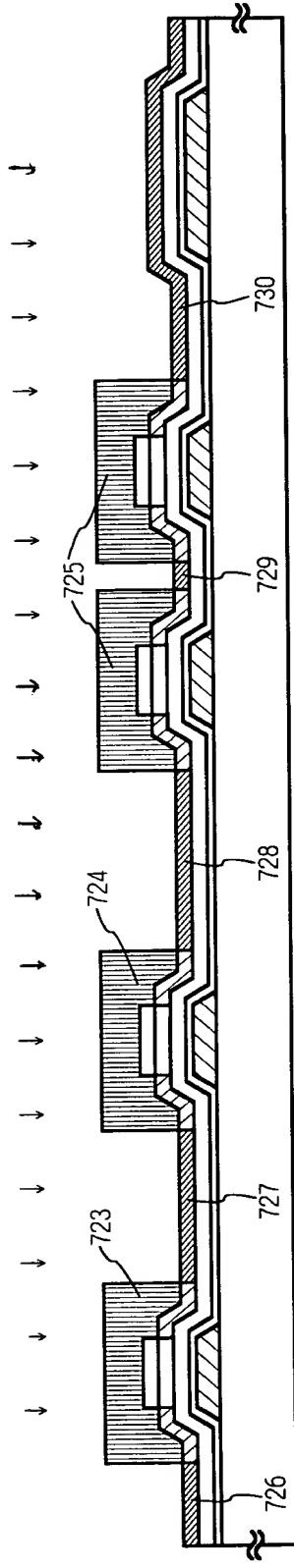
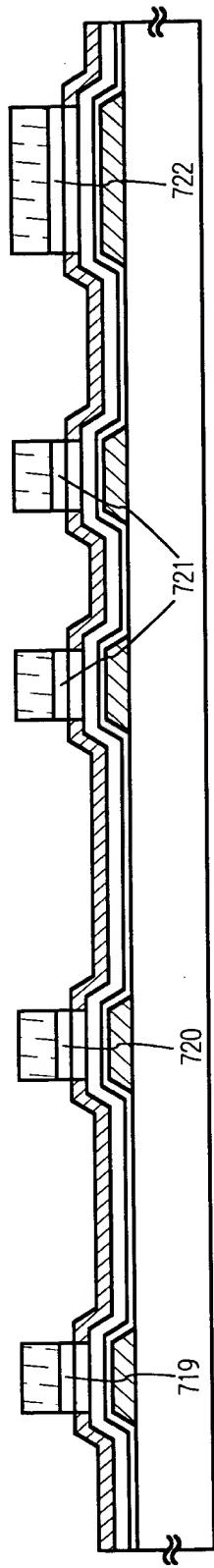


FIG. 6





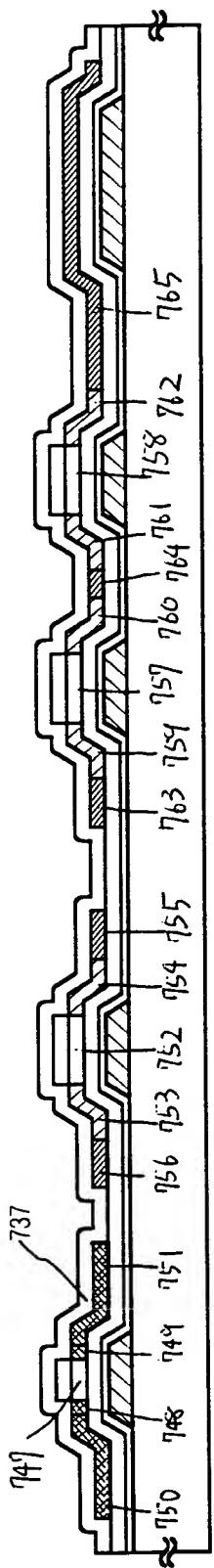


FIG. 9A

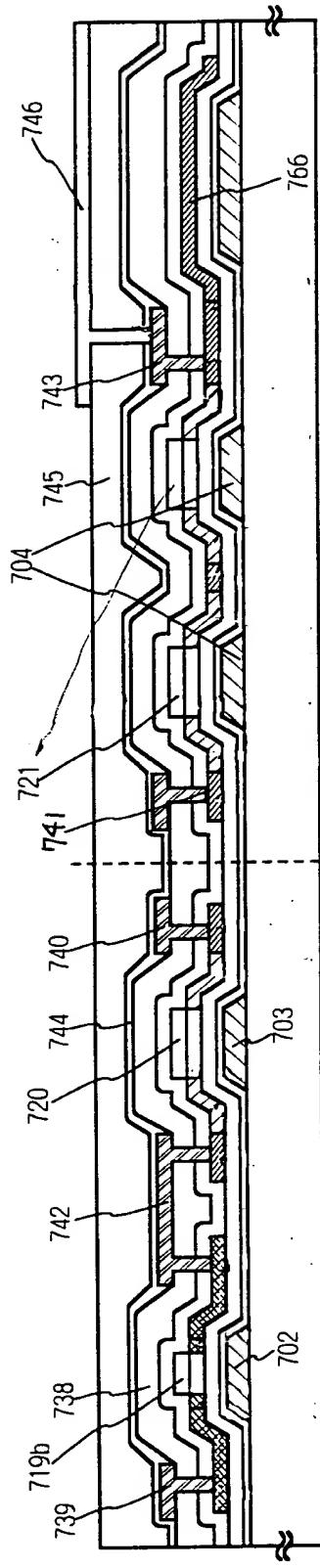


Fig. 9B

DRIVER CIRCUIT

169: P-CHANNEL TFT 170: N-CHANNEL TFT

1-CHANNEL (F) B

769: PIXEL TFT

170: CAPACITOR

PIXEL REGION

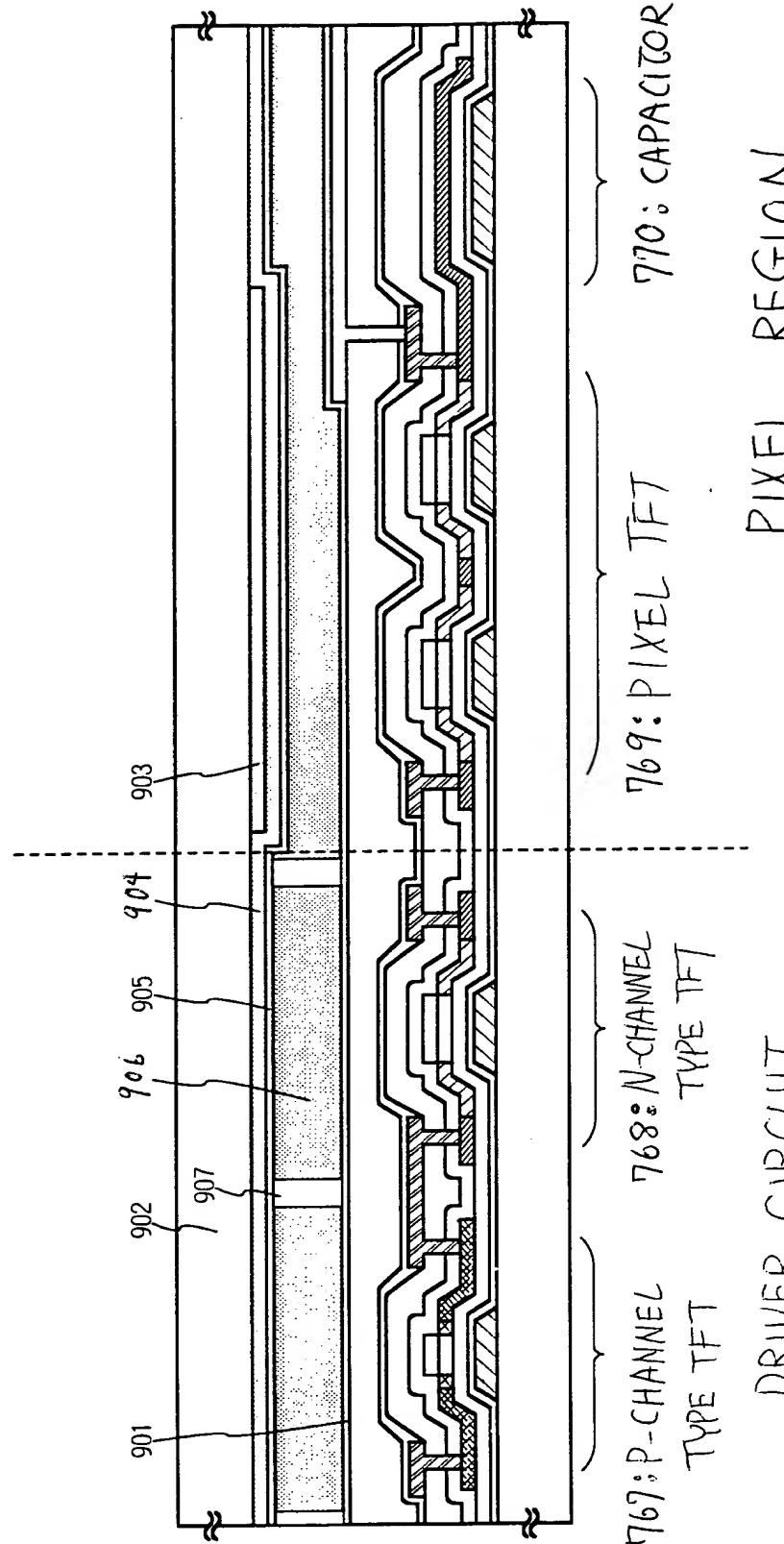


FIG. 10

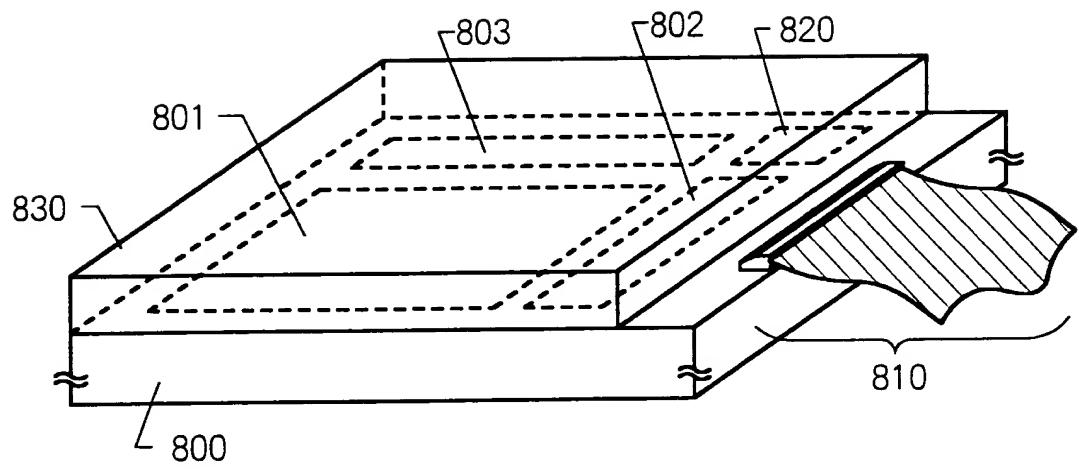


FIG. 1

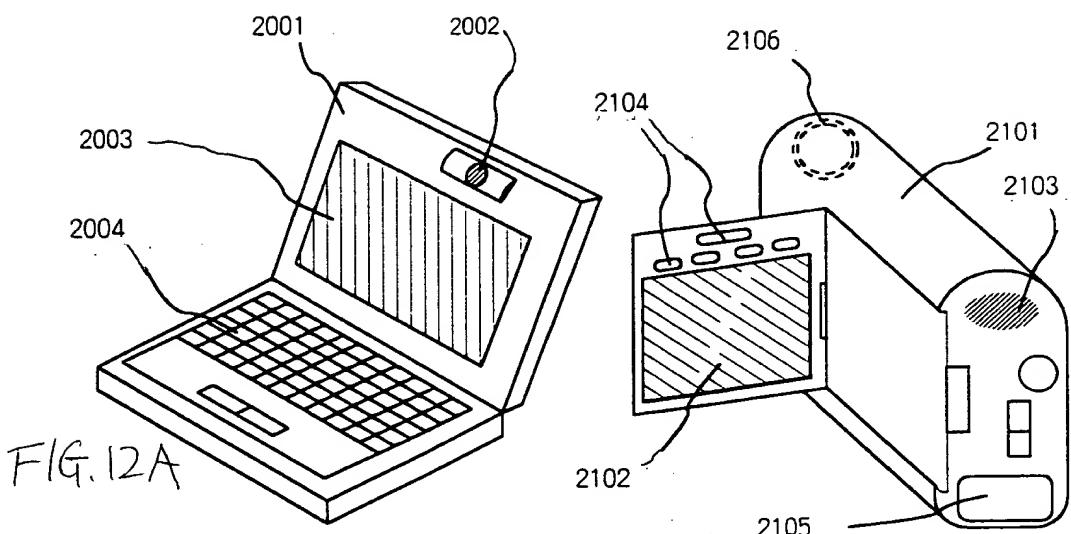


FIG. 12B

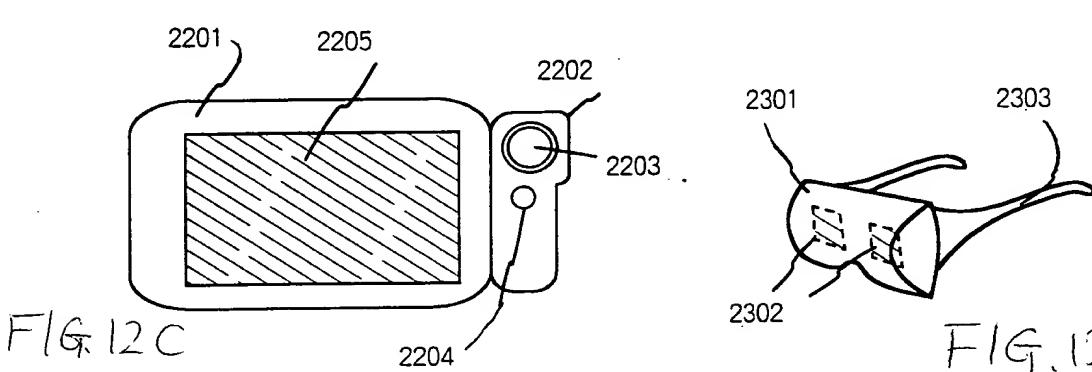


FIG. 12D

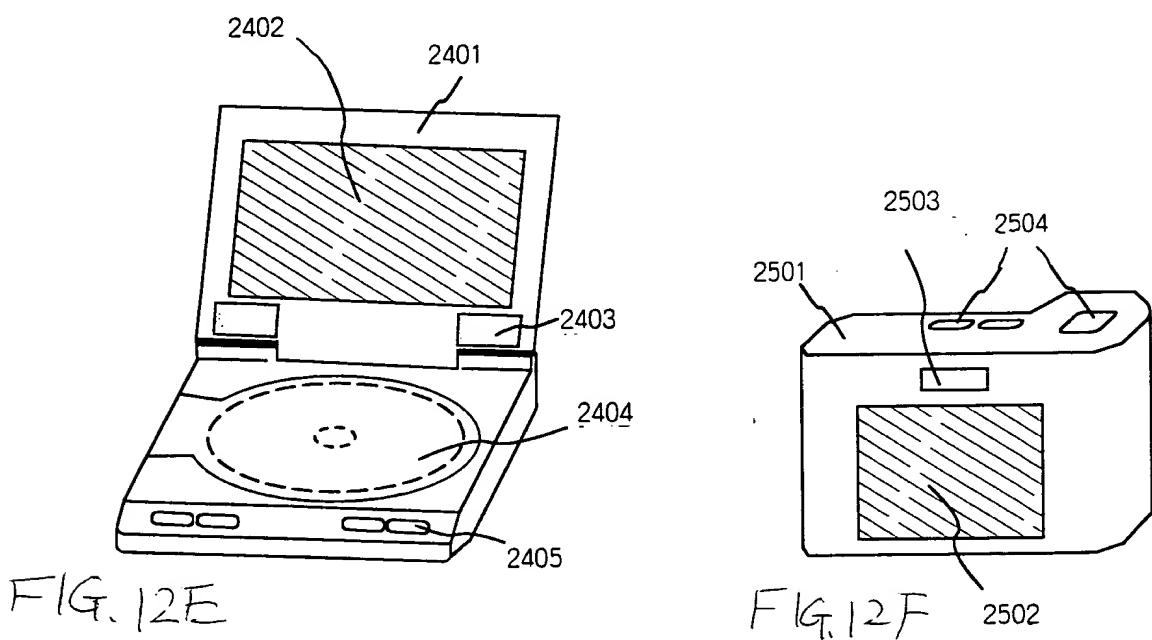


FIG. 12F

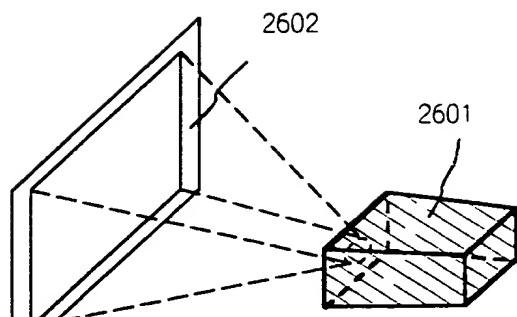


FIG. 13A

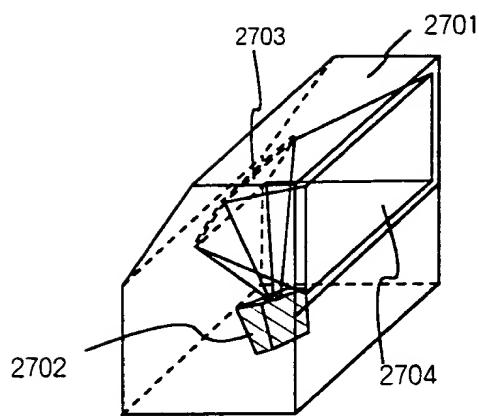


FIG. 13B

SCREEN

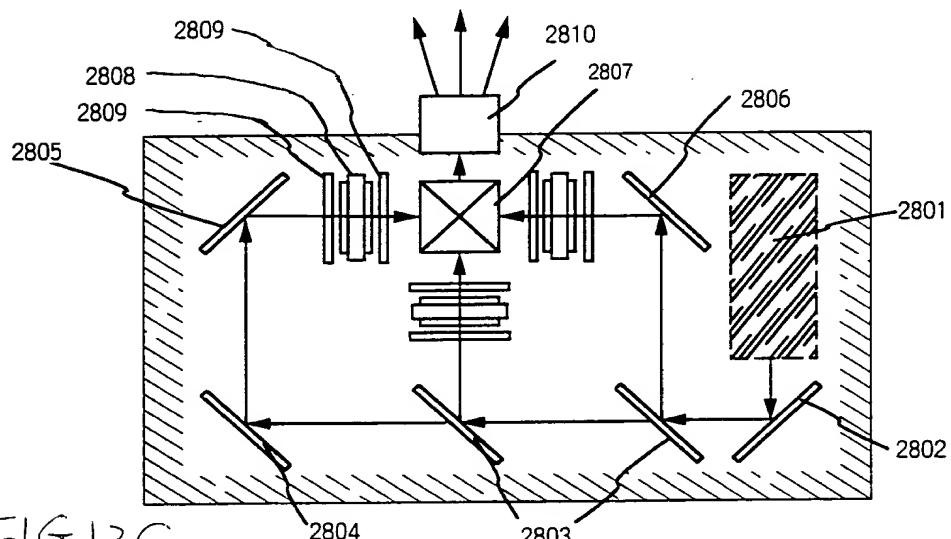


FIG. 13C

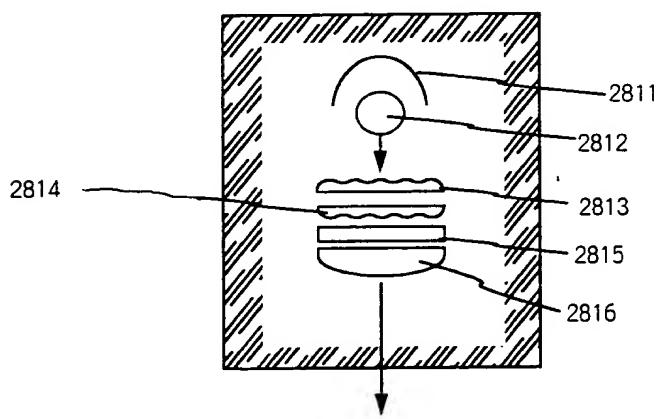


FIG. 13D

FIG. 14A

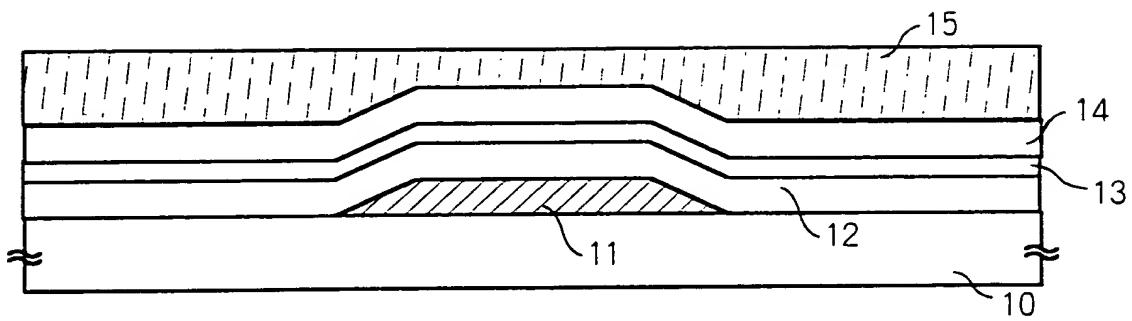


FIG. 14B

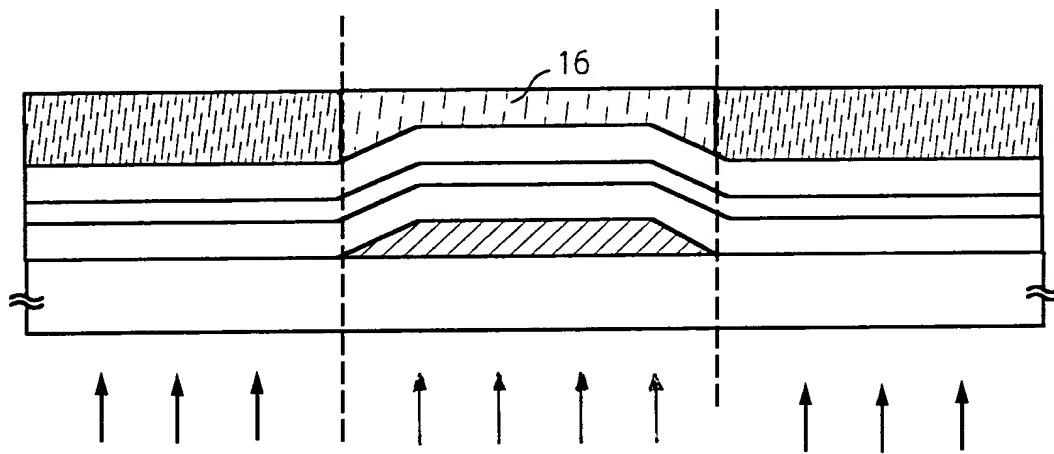
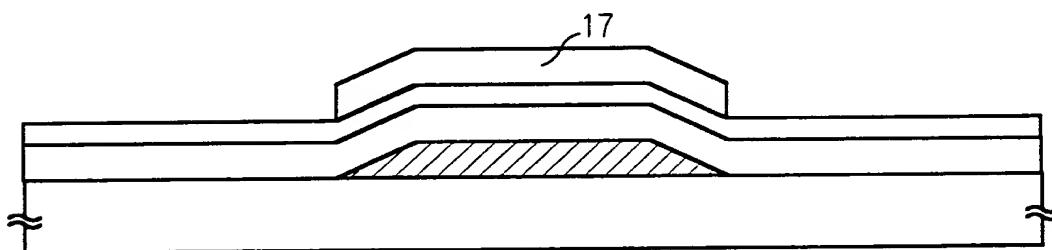


FIG. 14C



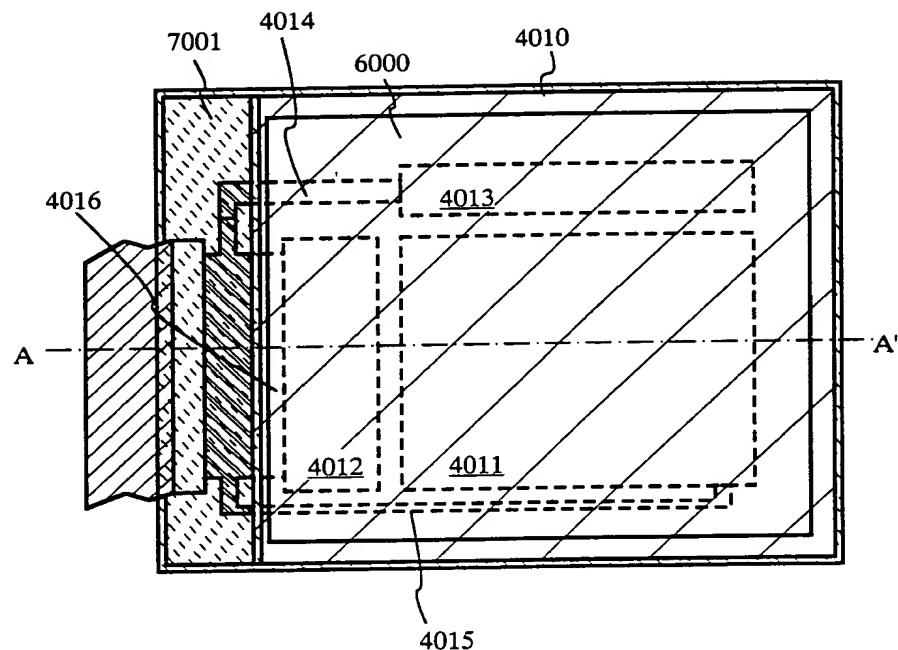


FIG. 15A

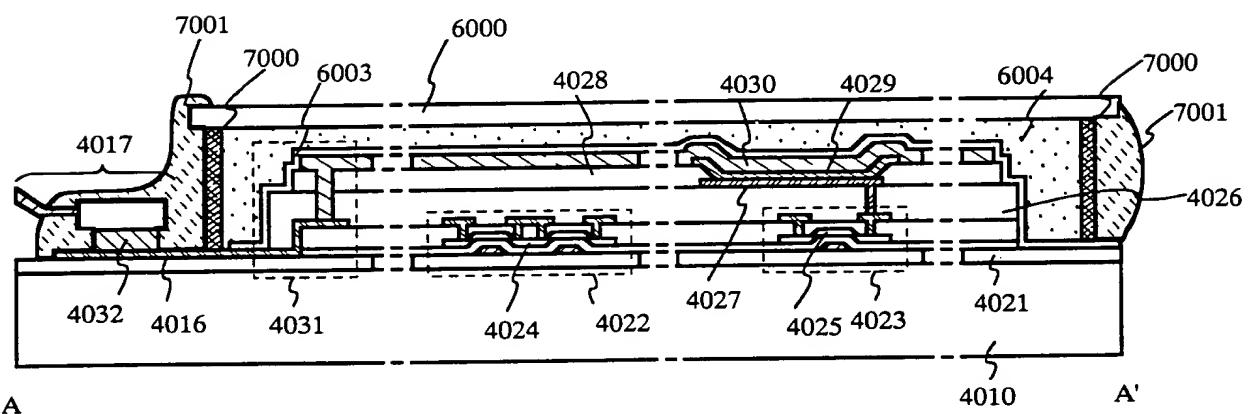


FIG. 15B

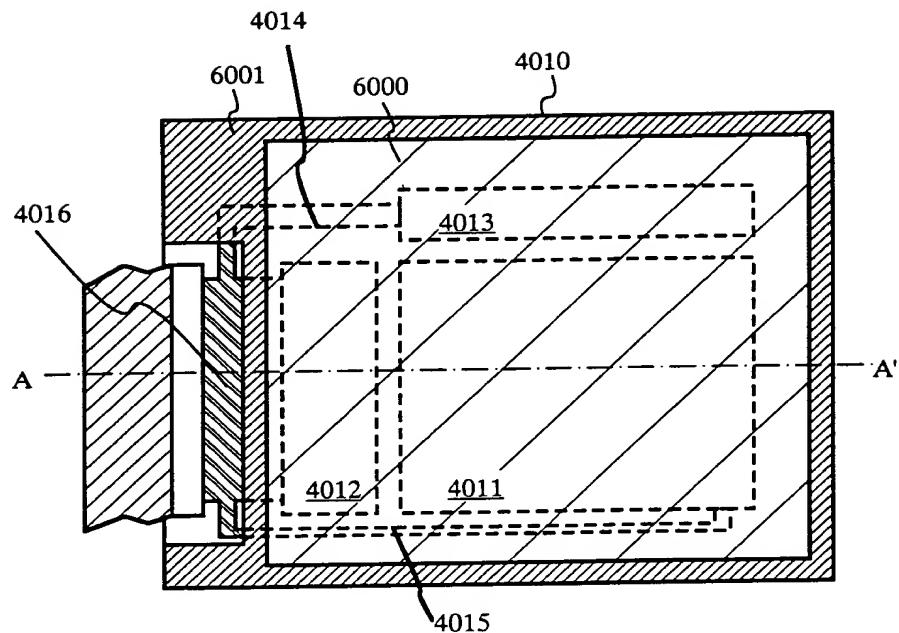


FIG. 16A

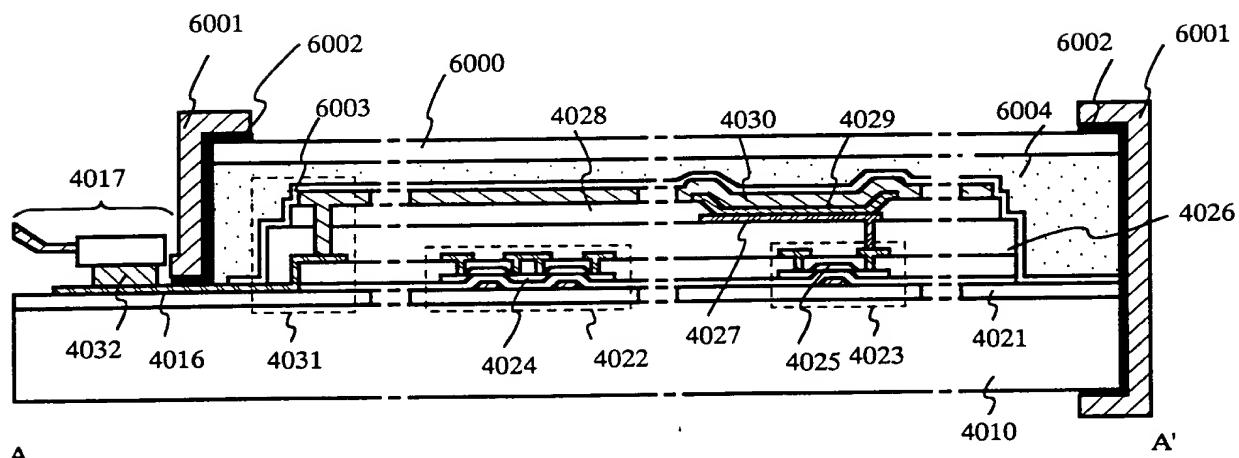


FIG. 16B

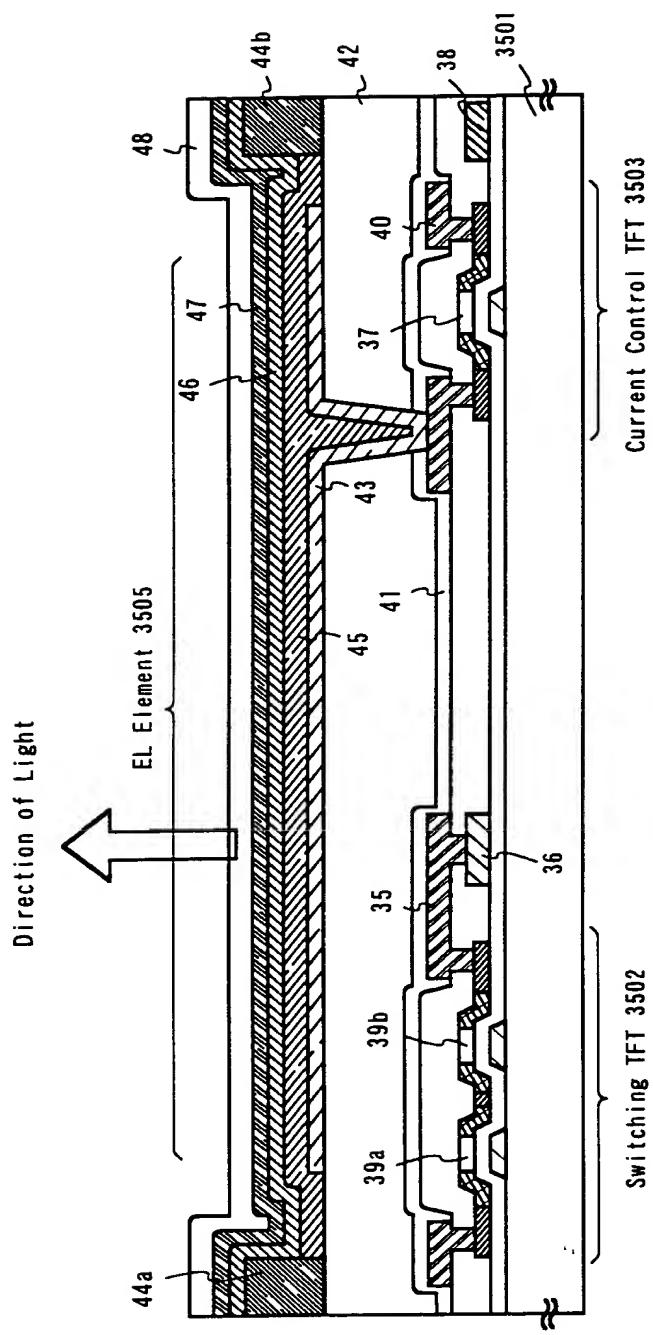


FIG. 17

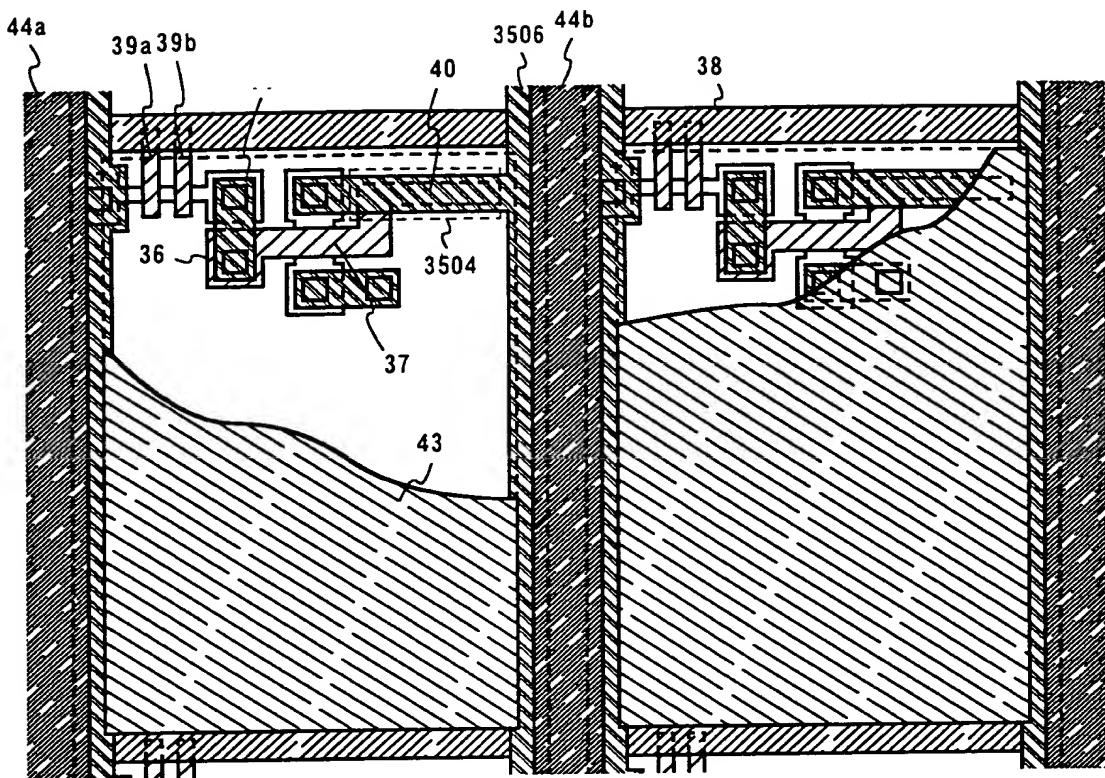


FIG. 18A

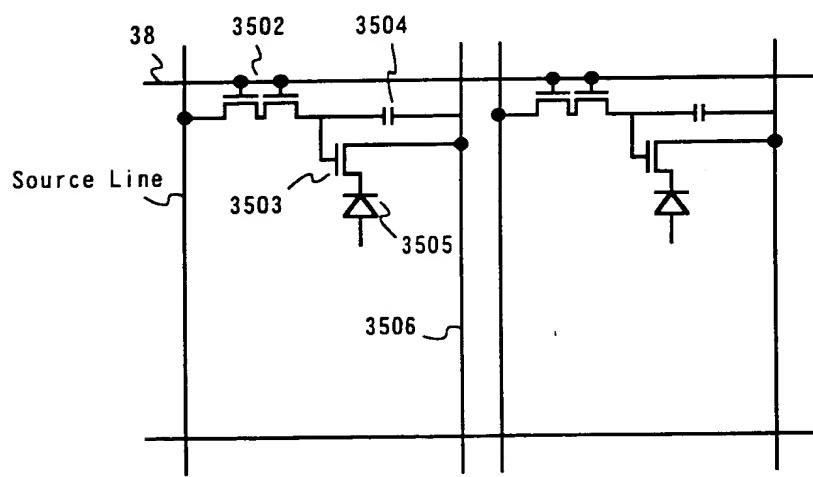


FIG. 18B

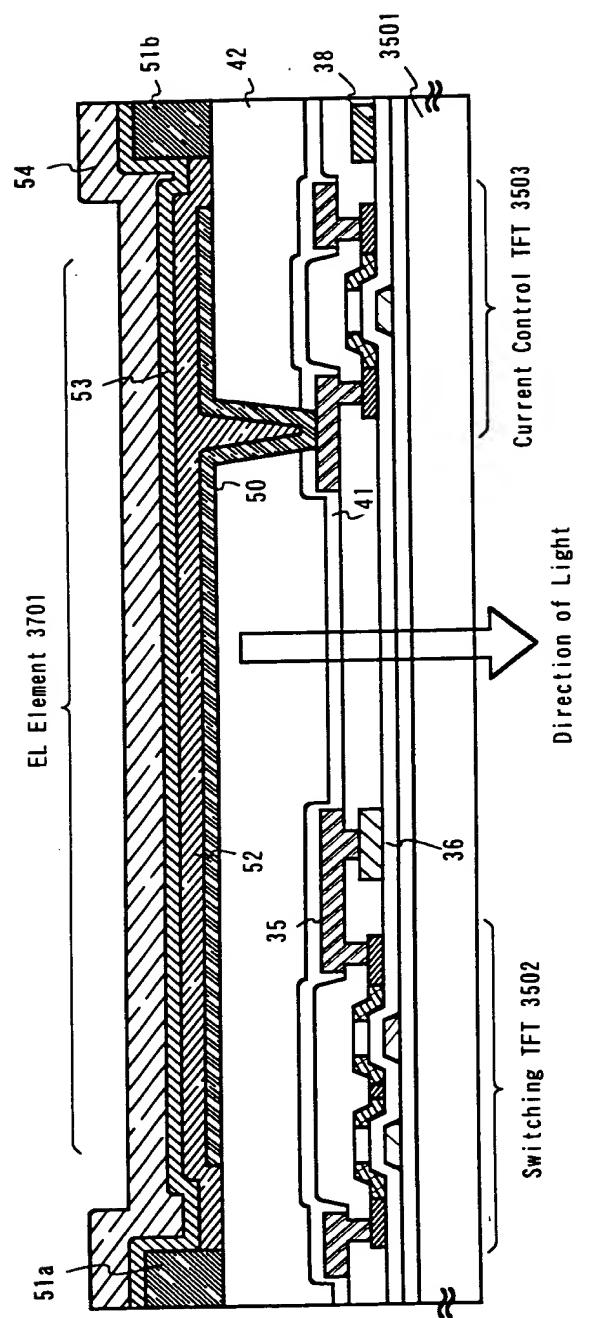


FIG. 19

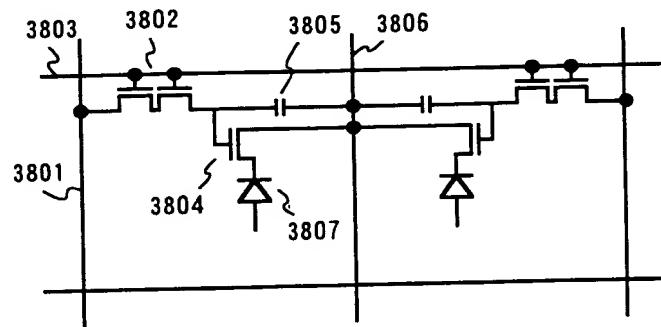


FIG. 20 A

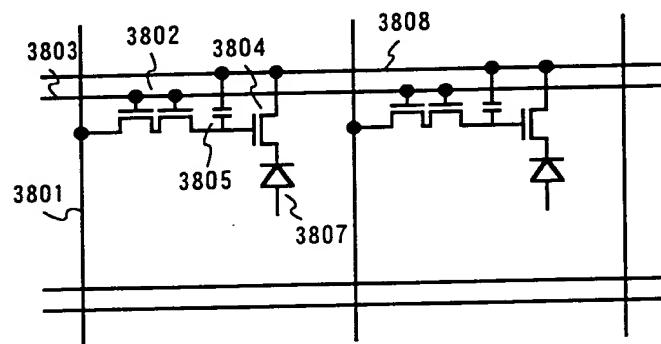


FIG. 20 B

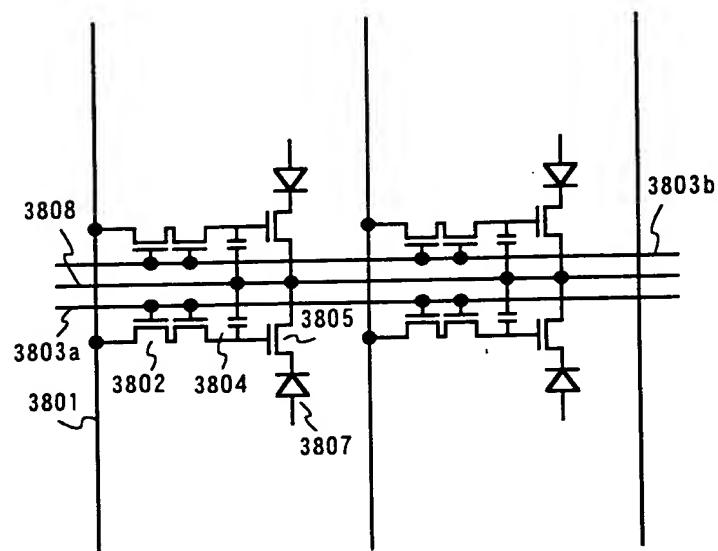


FIG. 20 C